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7204

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10053138	FILING DATE 01/18/2002	CLASS 438	SUBCLASS	GAU 1765	EXAMINER
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****APPLICANTS:** Savas Stephen;

****CONTINUING DATA VERIFIED:**
 THIS APPLICATION IS A CIP OF 09/860,698 05/16/2001 PAT 6,395,641 *
 WHICH IS A DIV OF 09/398,553 09/17/1999 PAT 6,253,704
 WHICH IS A CON OF 08/727,209 10/08/1996 PAT 5,983,828
 WHICH CLAIMS BENEFIT OF 60/005,288 10/13/1995
 (*)Data provided by applicant is not consistent with PTO records.

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**** FOREIGN APPLICATIONS VERIFIED:**

PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no	14912.786
Verified and Acknowledged Examiners's initials	
TITLE : Pulsed plasma processing of semiconductor substrates	

U.S. DEPT. OF COMH./PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	Total Claims
			Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	Applicant Examiner
		PREPARED FOR ISSUE	
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